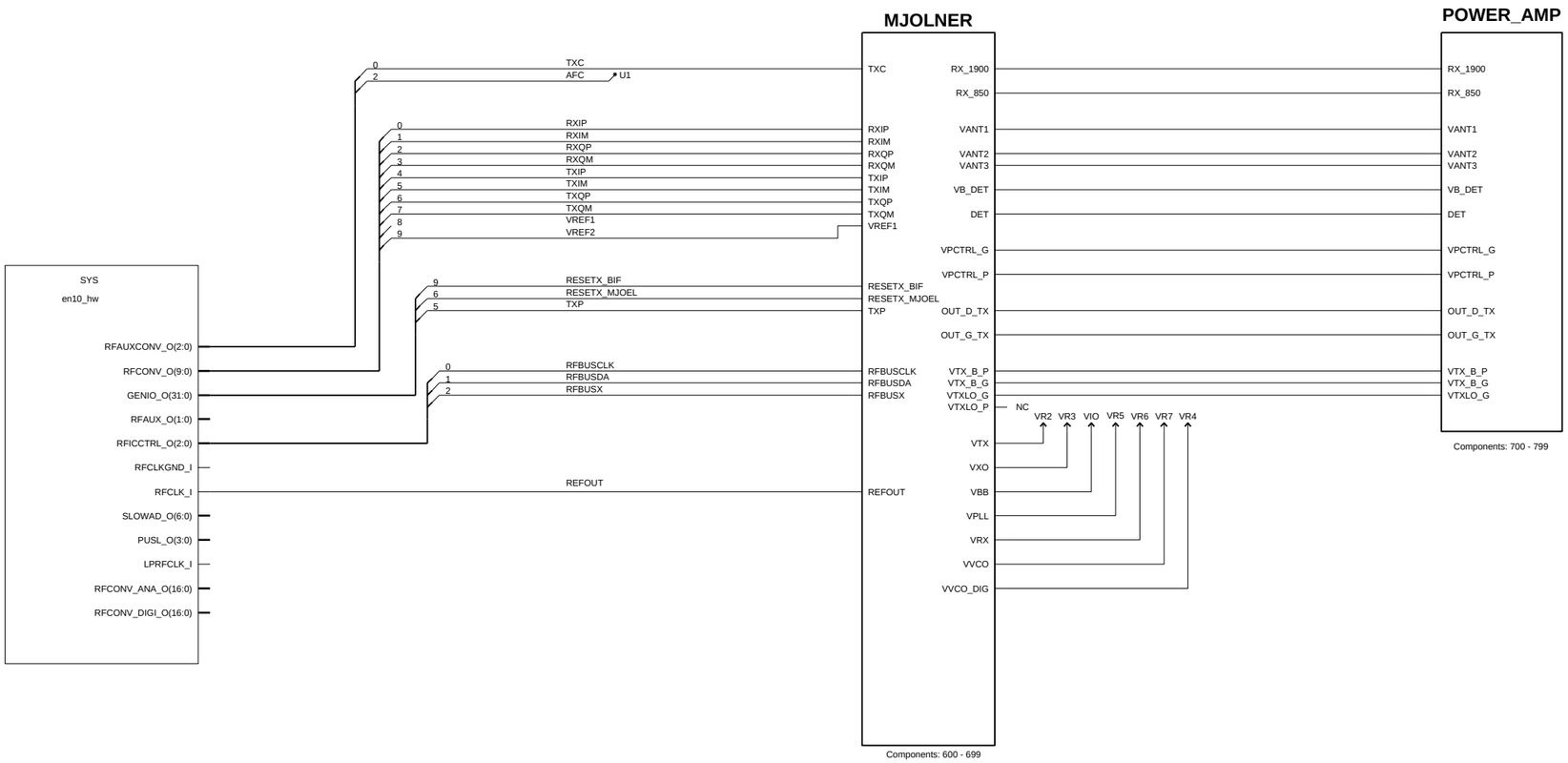
Top Level Schematic (FDK VCO)



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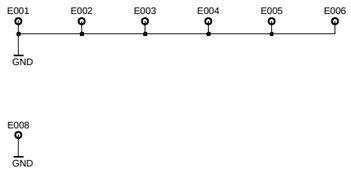
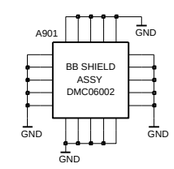
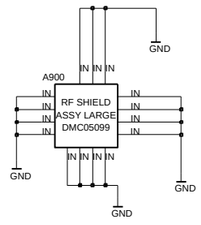
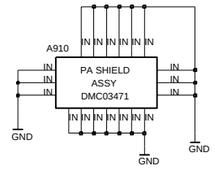
Shield Sheet for FDK VCO









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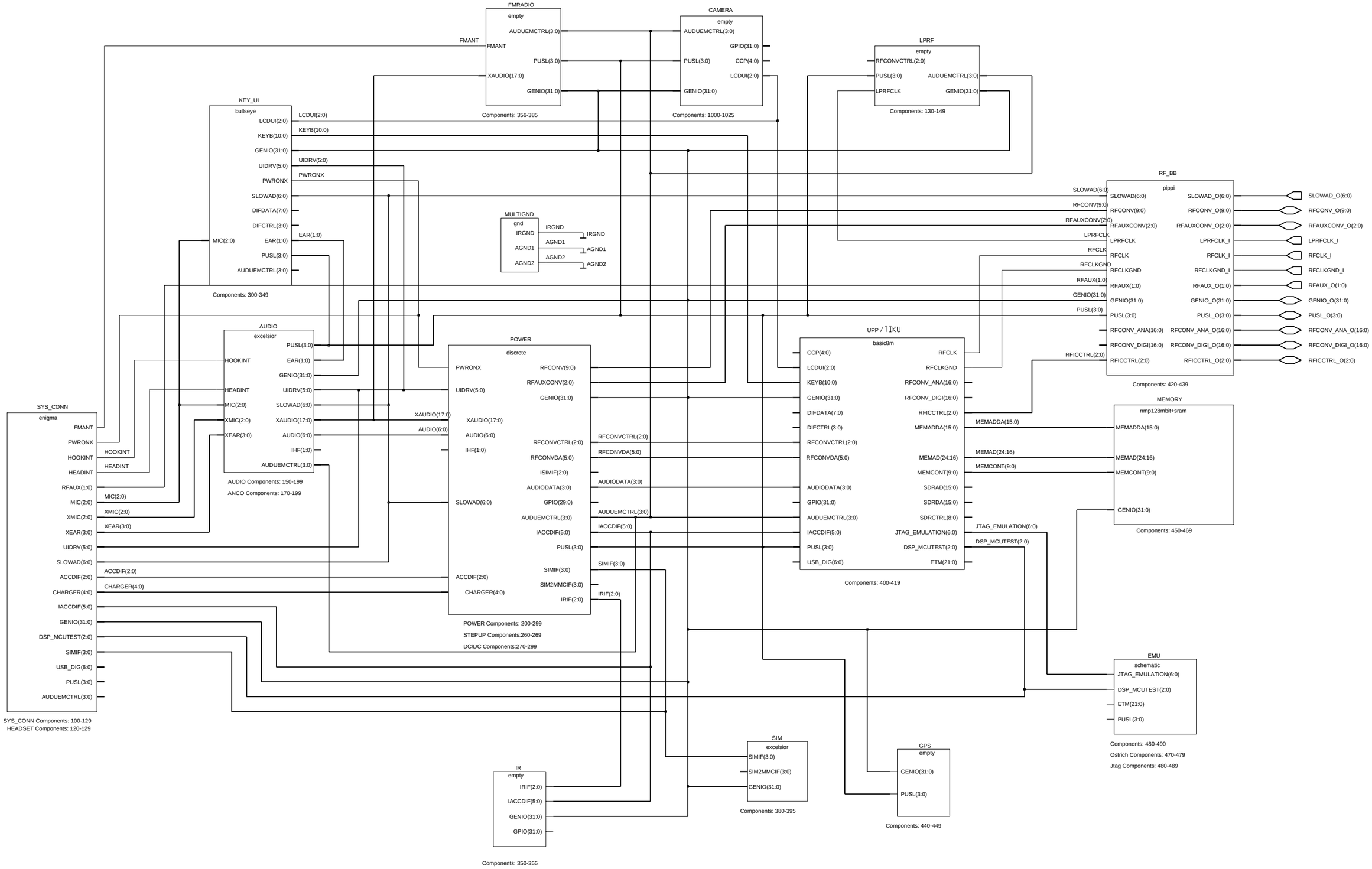
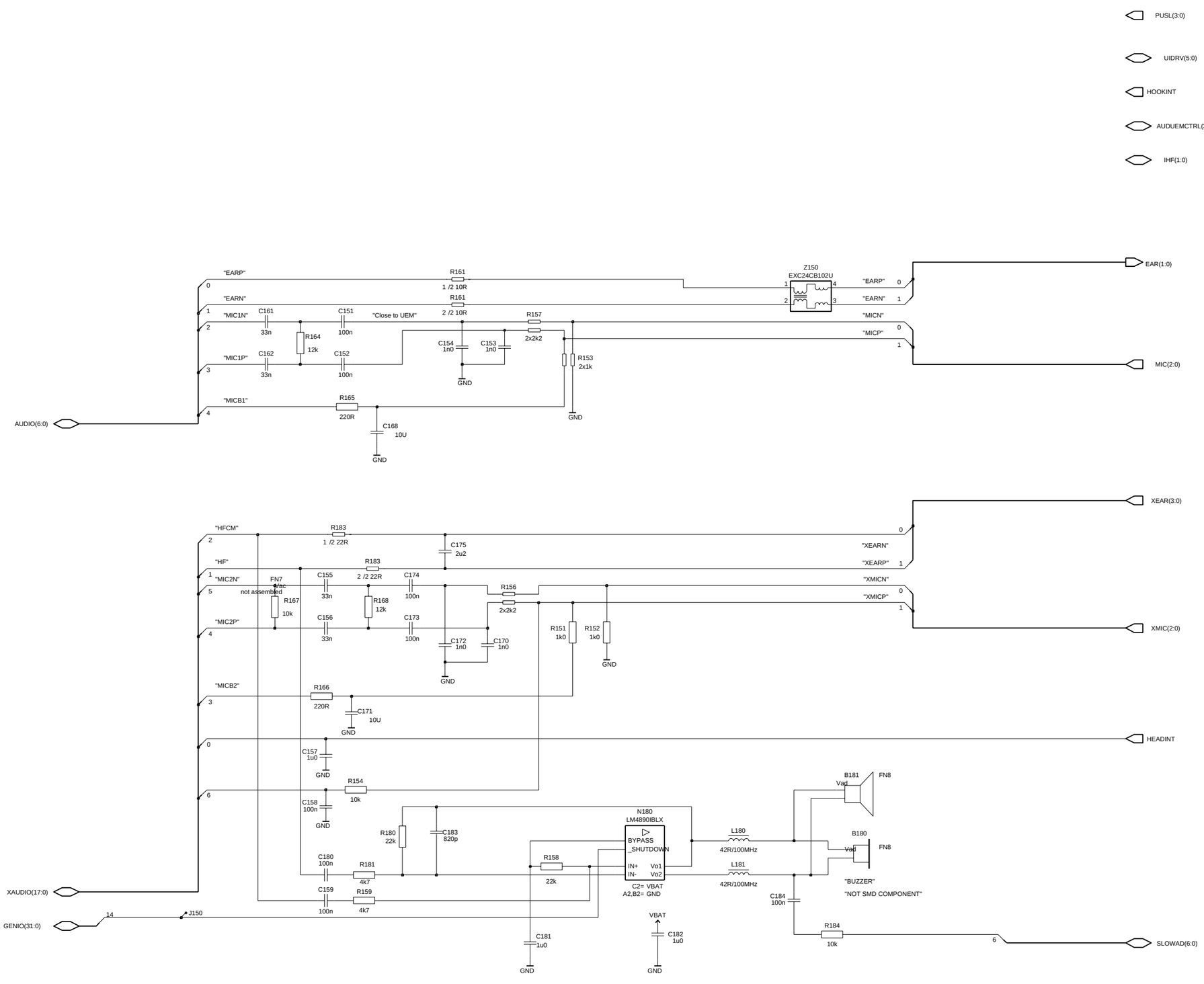
DCT4 Common Baseband Schematic (Top Level)



Audio Interface



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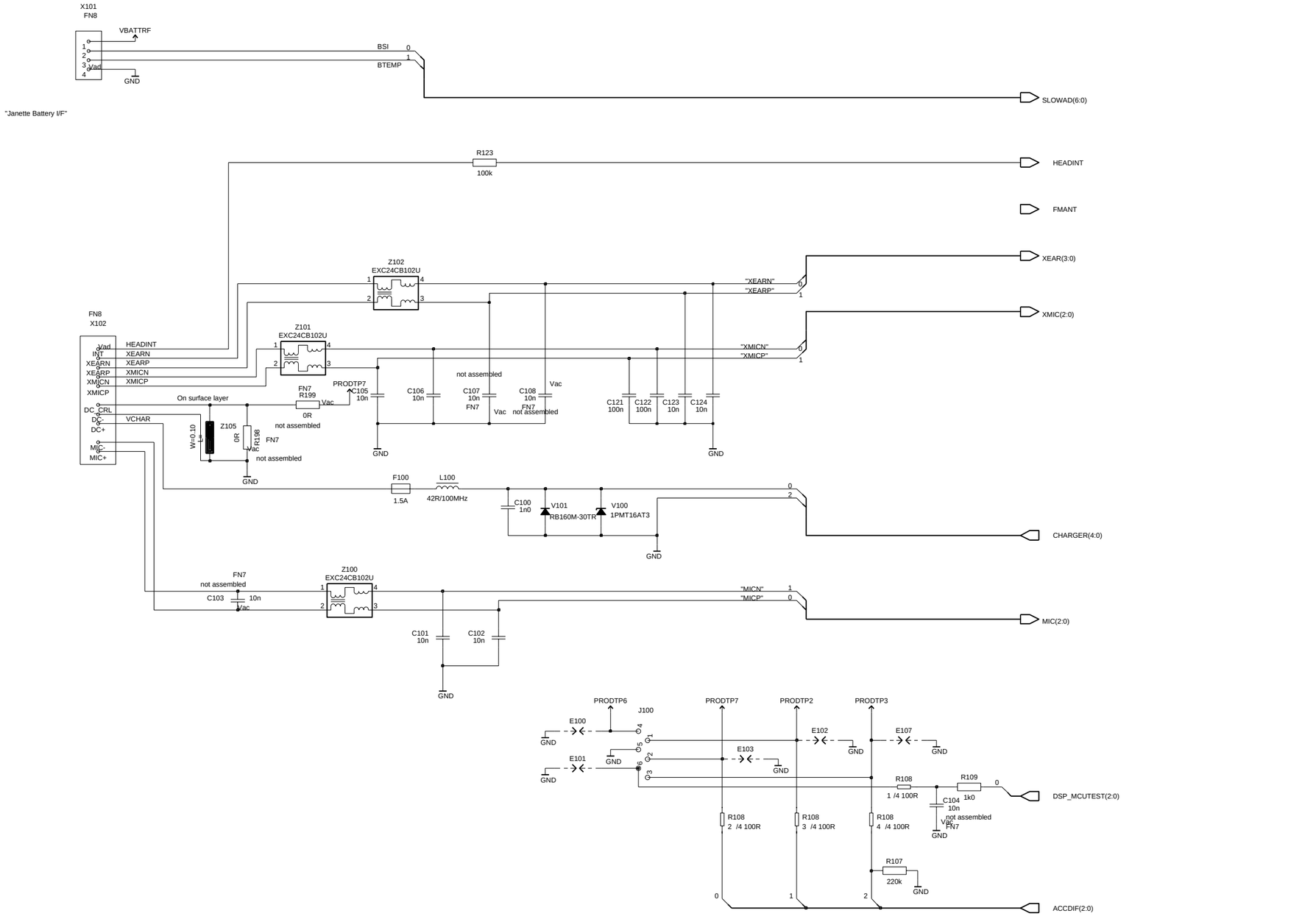
System Connector



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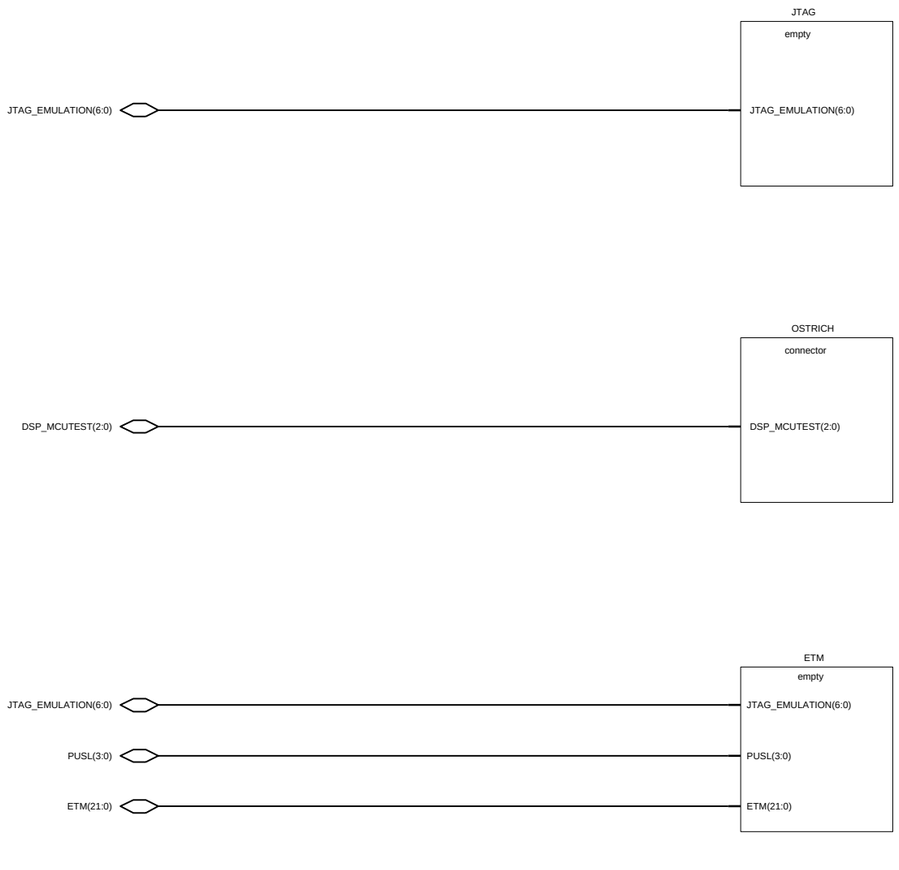
Test and Emulator Interface



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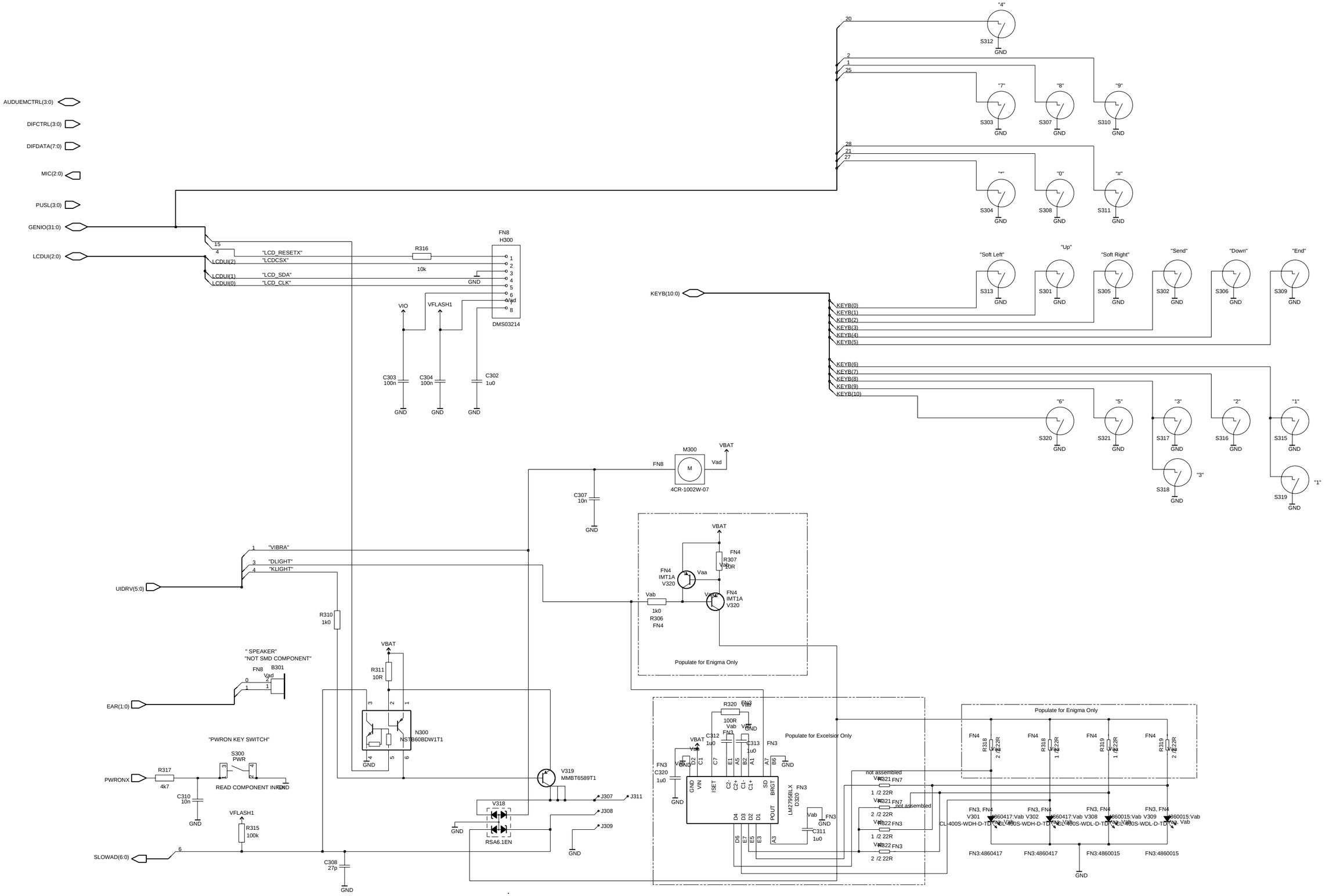
User Interface



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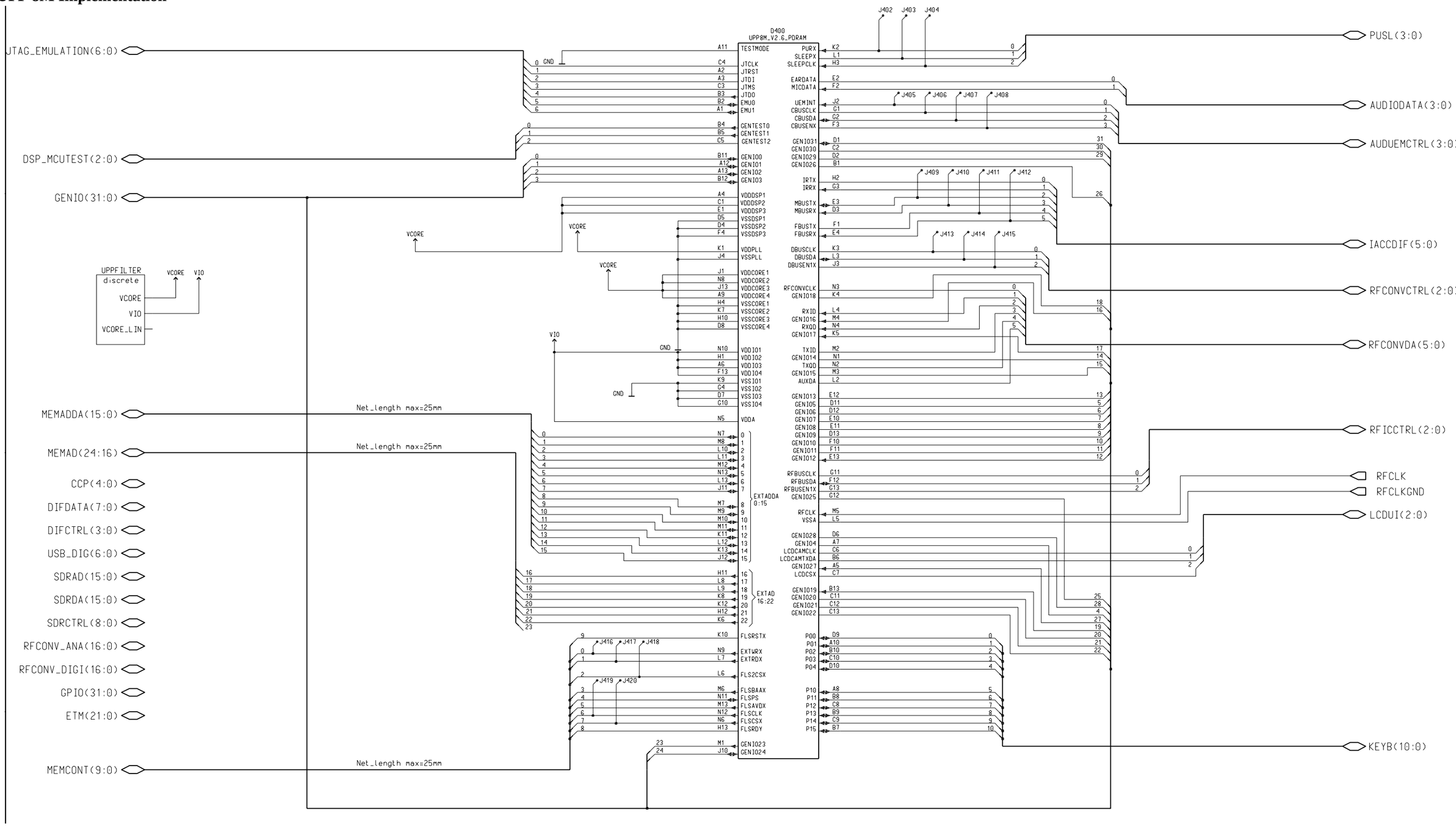
UPP 8M Implementation



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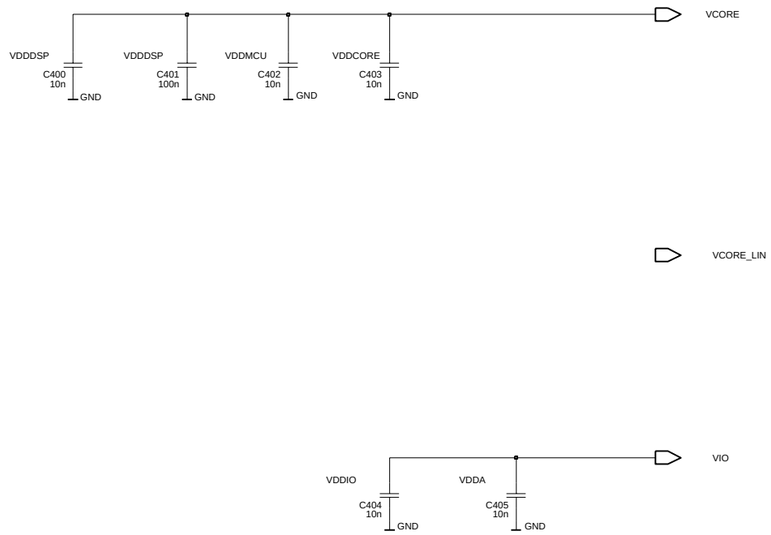
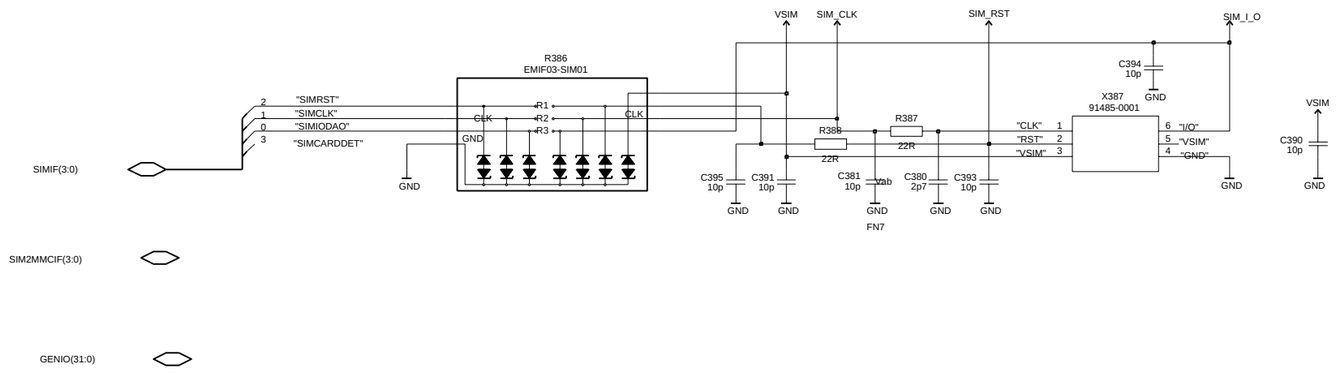
Discrete Decoupling Capacitors for UPP



SIM Reader



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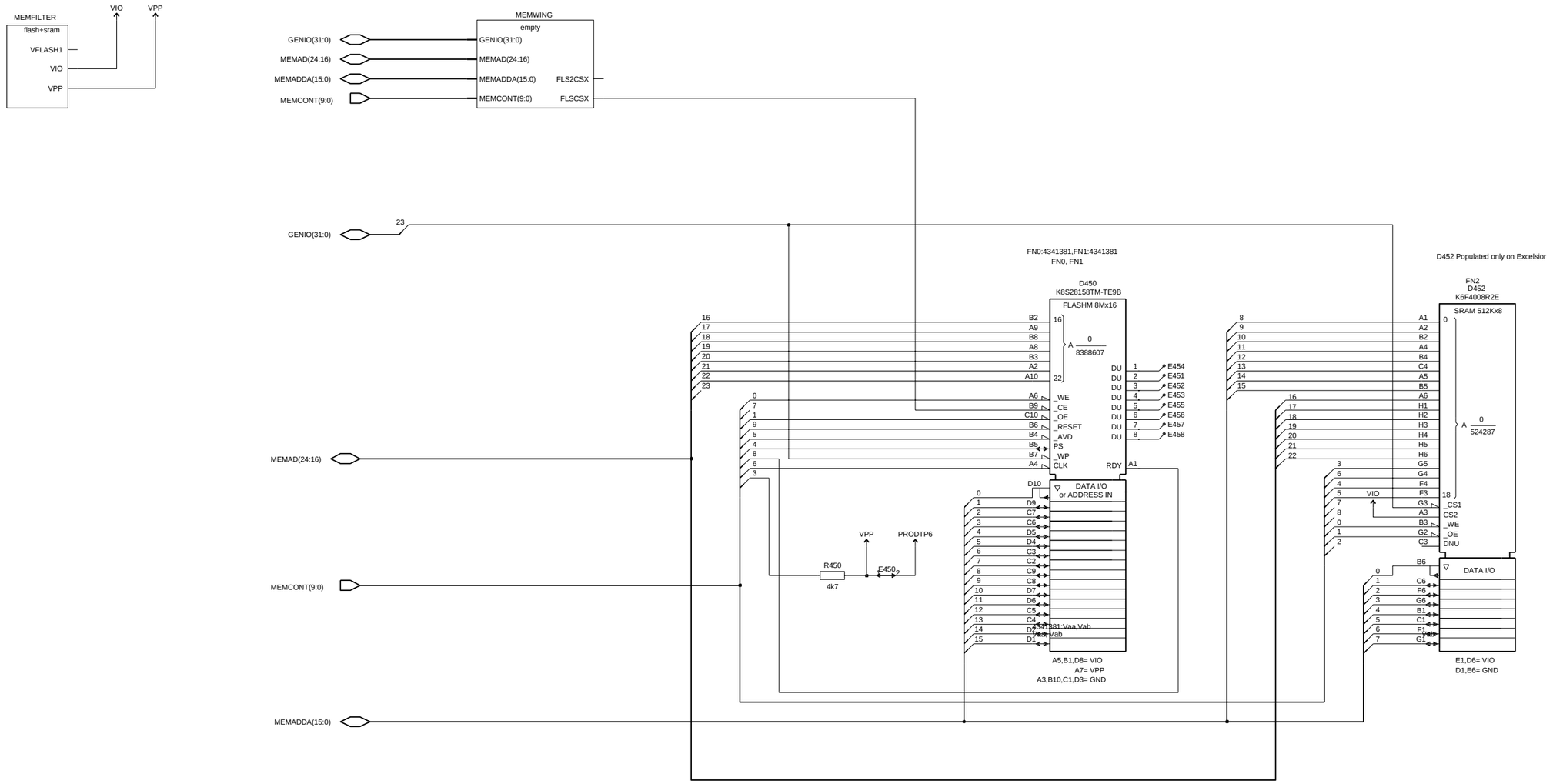
Intel 128 Mbit + 4 Mbit SRAM



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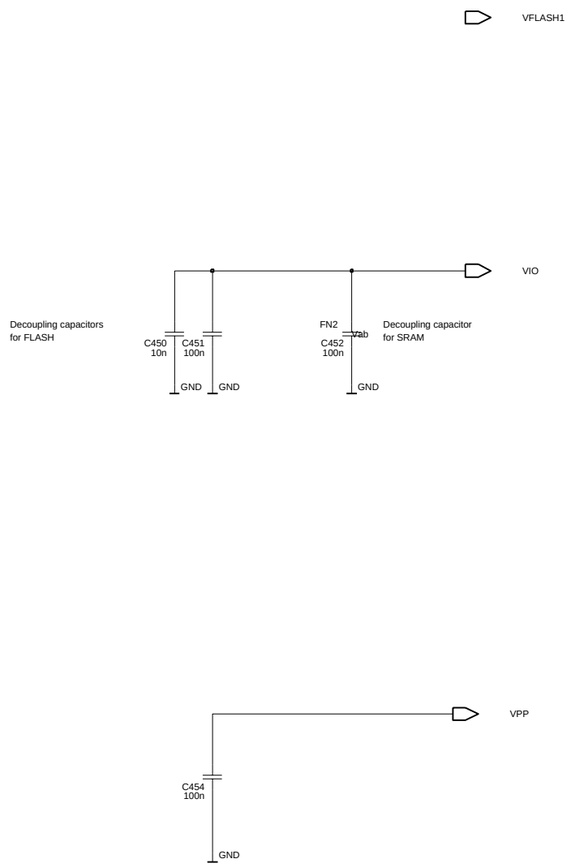
Recoupling Capacitors for Flash and SRAM



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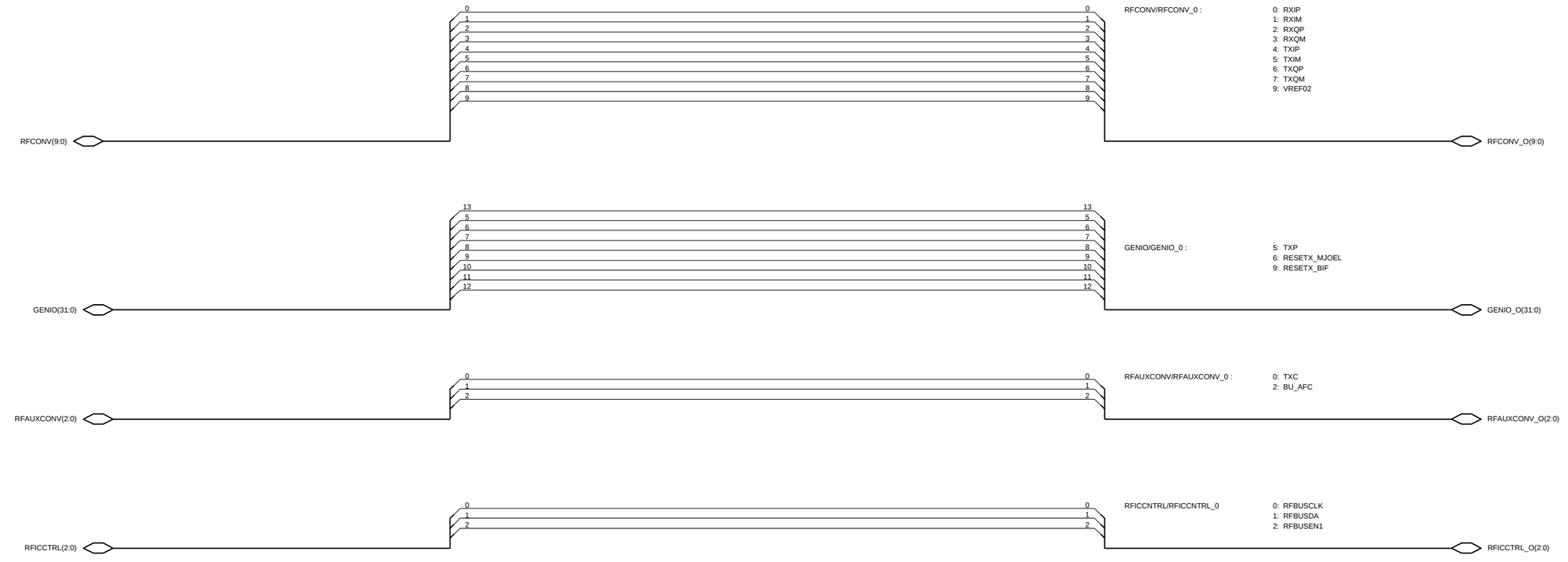
RF/BB Clark Sheet



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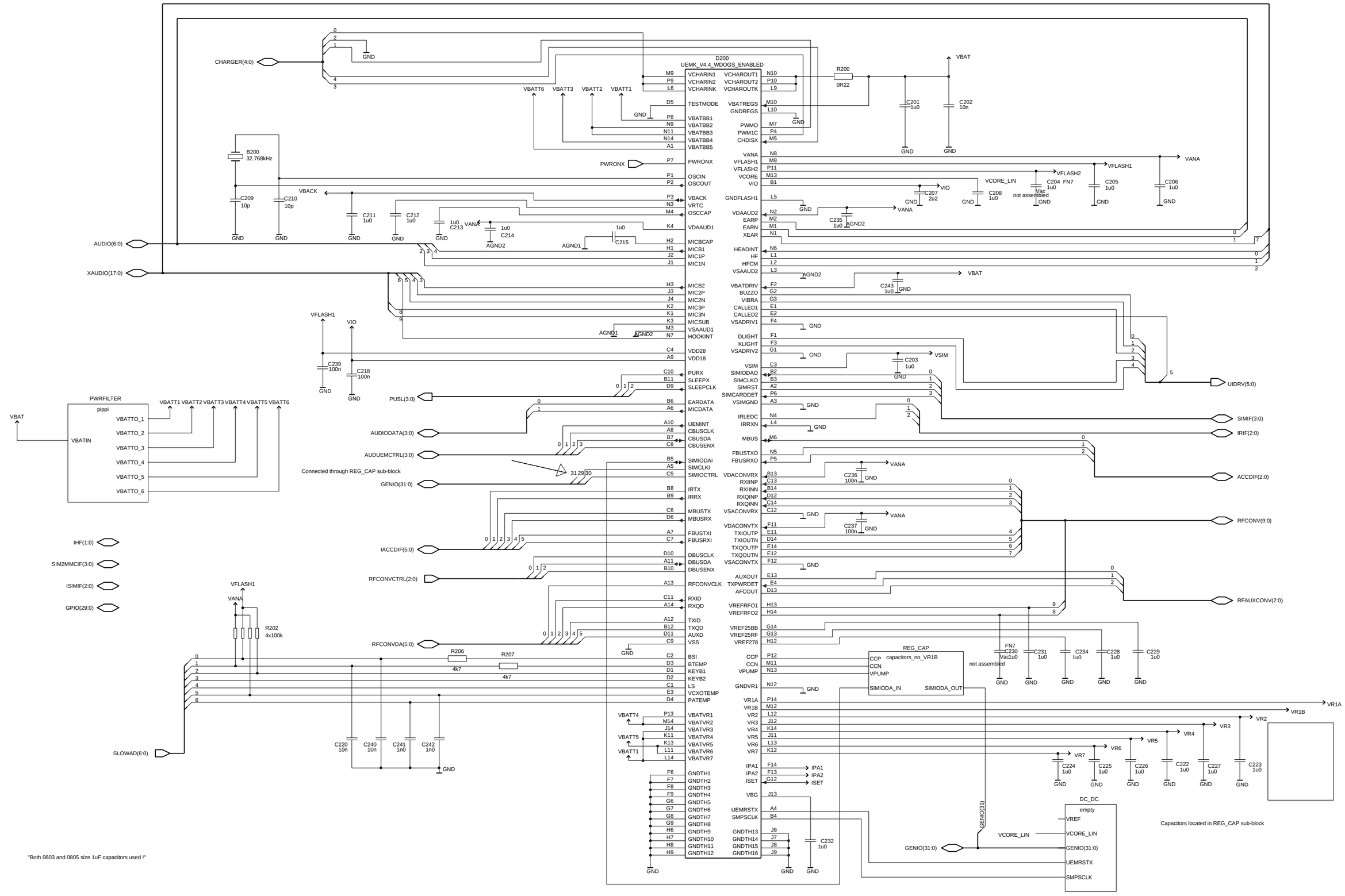
Discrete Power Management



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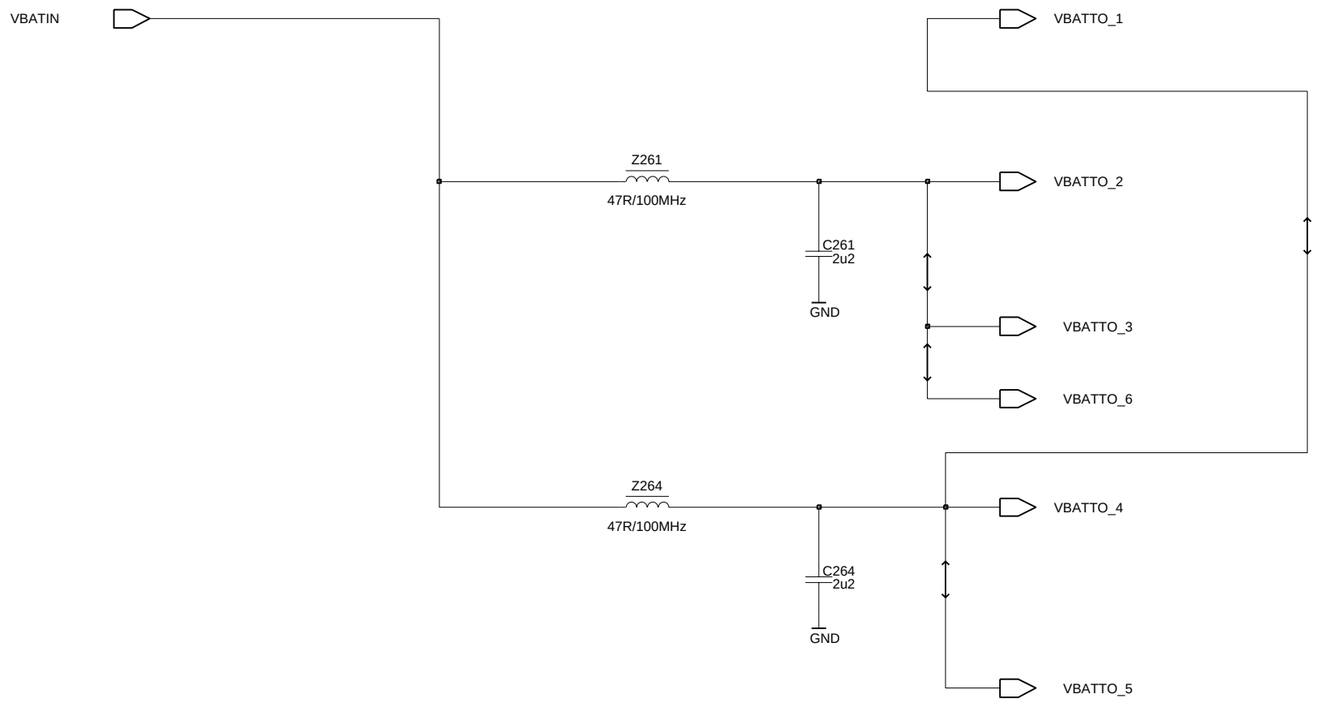
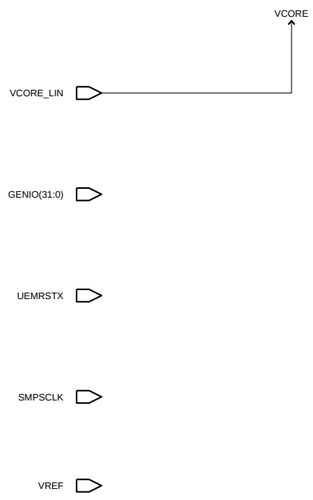
Light Filtering



DC/DC Converter



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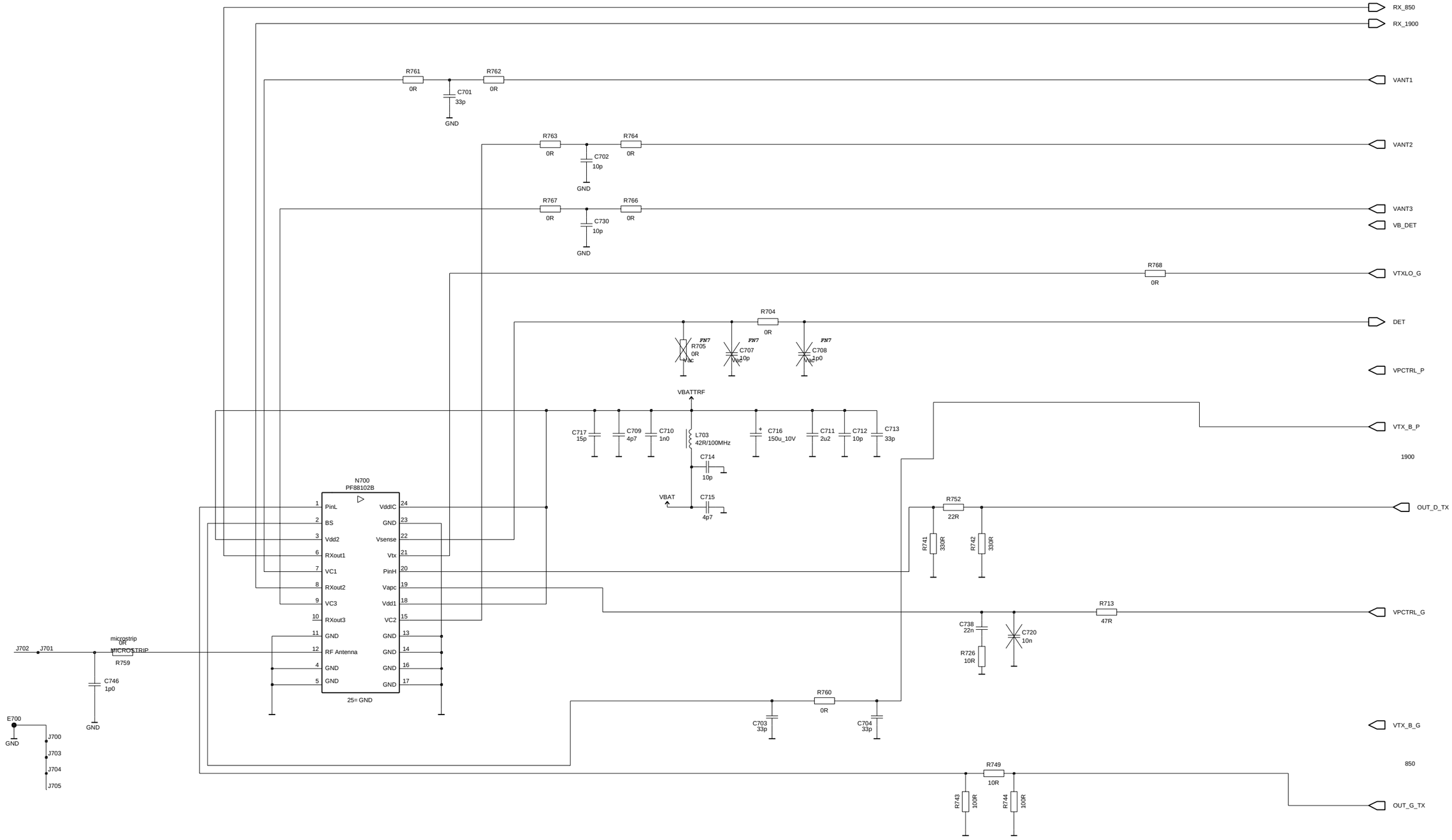
Power Amplifier



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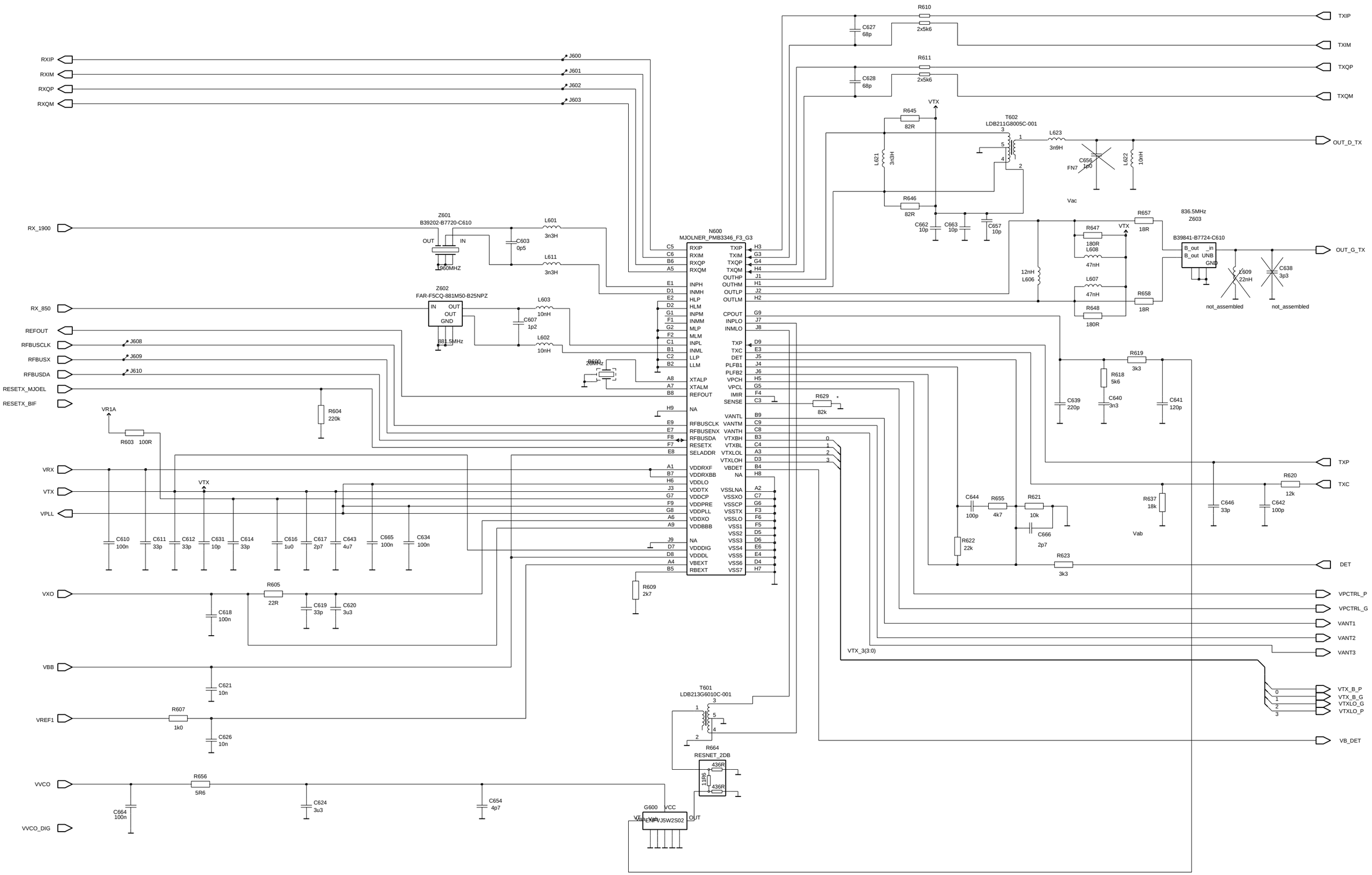

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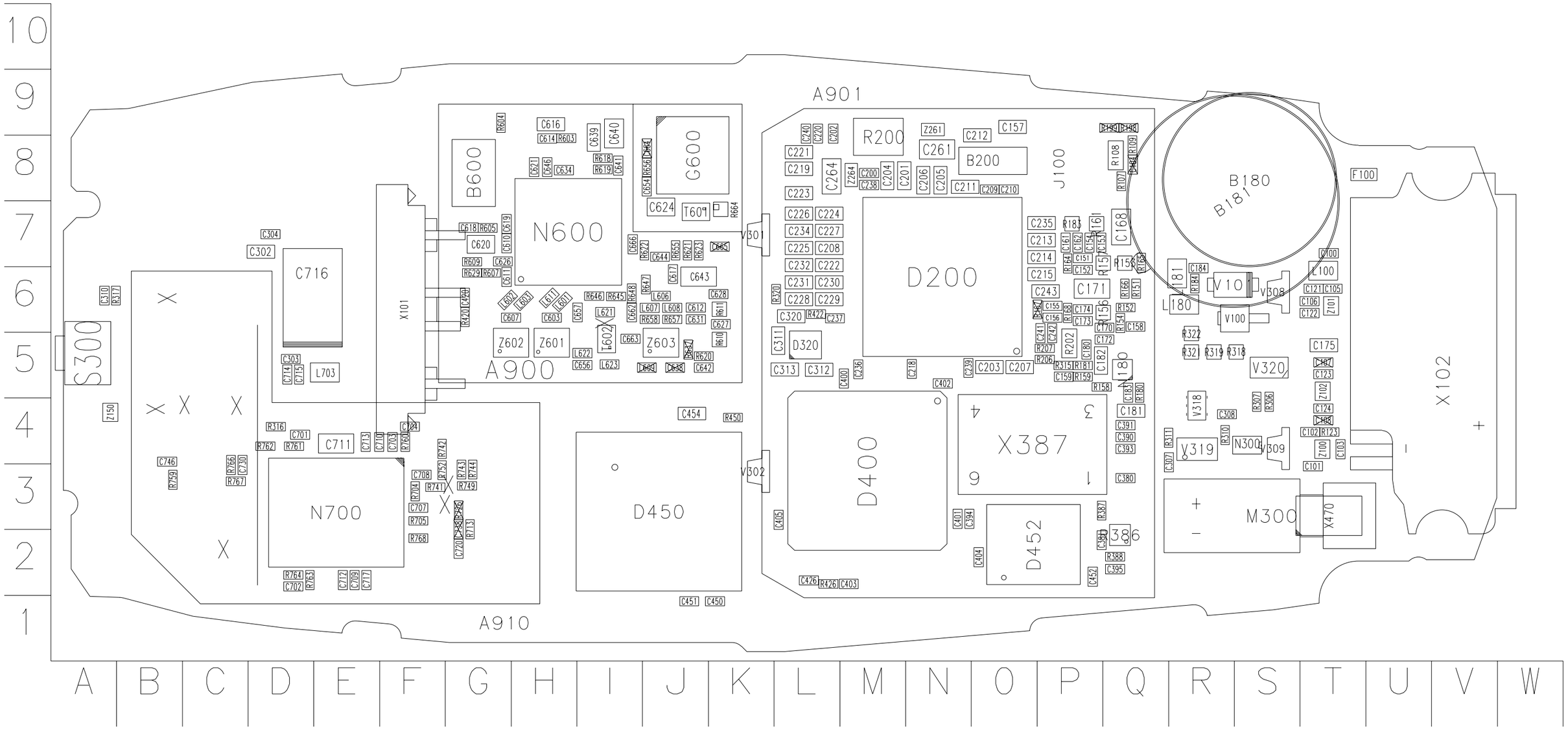
Component Layout (3595)- Top



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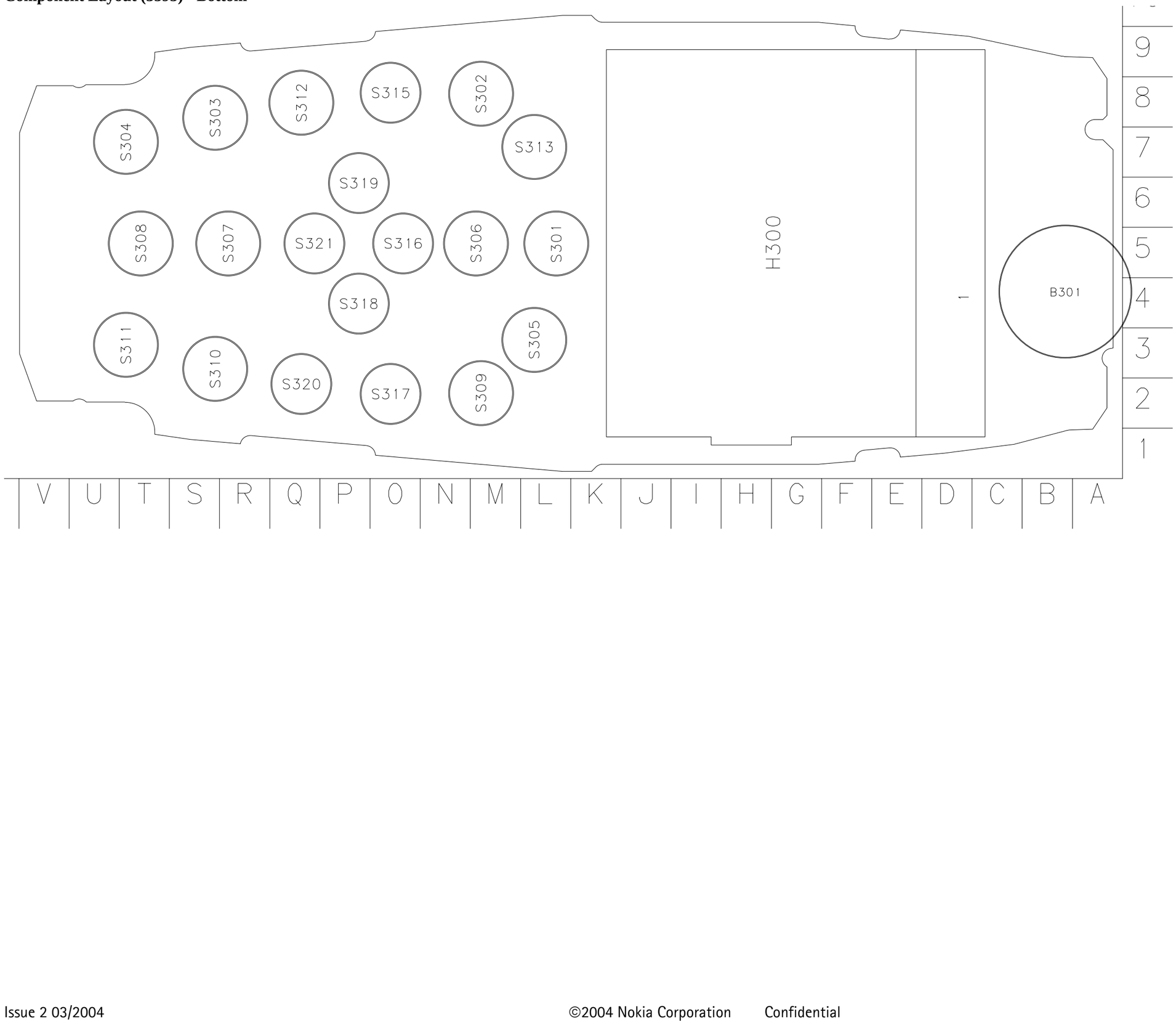
Component Layout (3595) - Bottom



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